

Remark 4. We note that every clause of $G_m(Y)$ contains the pure literal u . Therefore if pure literal elimination were run during preprocessing by a CDCL solver then it would simply remove the clauses of $G_m(Y)$. However, it is not hard to modify $G_m(Y)$ by appending extra clauses H obtained by adding \bar{u} to an arbitrary satisfiable formula on a new small set of extra variables that does not contain any pure literals. The pure literal preprocessing would then not prune the clauses of $G_m(Y)$ and the initial branch setting $u = 0$ would immediately satisfy these extra clauses H which would eliminate their impact on the CDCL execution in any of our arguments.

Simulating Resolution Without Restarts

We begin with the observation that if a given refutation P is too large, the simulation can simply discard it and instead start with an obvious tree-like resolution refutation with at most $2^n - 1$ derived clauses.

Lemma 2. *Let P be a given resolution refutation of $F_n(X)$. If $\text{length}(P) > (2^n - 1)/n^2$, then there is a non-restarting CDCL solver that, even without employing clause learning, when run on $F_n(X)$ produces a tree-like resolution refutation after fewer than $n^2 \cdot \text{length}(P)$ branching decisions.*

Proof. Let S be a non-restarting CDCL solver that simply discards P and instead simulates an obvious tree-like resolution refutation P' (w.r.t. any fixed variable order) of $F_n(X)$. P' essentially enumerates the 2^n possible variable assignments σ and identifies a clause of $F_n(X)$ (or a learned clause, if S uses clause learning) falsified by each such σ . P' will need no more than $2 \cdot (2^{n-1} - 1) = 2^n - 2$ branching decisions as the $(n - 1)$ -st assignment for any σ will either satisfy all clauses of $F_n(X)$ or unit propagate the remaining variable. By the precondition on the length of P , the number of branching decisions is thus smaller than $n^2 \cdot \text{length}(P)$. \square

In the rest of this section, we will thus assume w.l.o.g. that $\text{length}(P) \leq (2^n - 1)/n^2$. Pipatsrisawat and Darwiche (2011) provide a CDCL simulation of P , henceforth referred to as the *PD simulation*, that uses $R \leq n^2 \cdot \text{length}(P)$ restarts and has size $O(n^4 \cdot \text{length}(P))$. By the above observation regarding $\text{length}(P)$, we have $R \leq (2^n - 1)$. Applying Theorem 1 using a counter with $m = n$ bits thus yields the following:

Corollary 1. *Let P be a resolution refutation of $F_n(X)$. Then there is a non-restarting CDCL solver that infers any one asserting clause per conflict when run on formula $F_n(X) \wedge G_n(Y)$ produces a resolution refutation of $F_n(X)$ of size $O(n^4 \cdot \text{length}(P))$.*

We can strengthen this by using a variant of the PD simulation together with a somewhat sharpened analysis.

Theorem 2. *Let P be a resolution refutation of $F_n(X)$. Then a resolution refutation of $F_n(X)$ of length $O(n^2 \cdot \text{size}(P))$ may be found using only $O(n^2 \cdot \text{size}(P))$ variable assignments by a non-restarting CDCL solver that infers any one asserting clause per conflict when run on formula $F_n(X) \wedge G_n(Y)$.*

Algorithm 1: p-simulation of a resolution refutation by a CDCL solver with simple preprocessing

Input : CNF formula $F_n(X)$ with a resolution refutation $P = (C_1, \dots, C_s)$
Output: A resolution refutation Q of $F_n(X)$

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1 begin
2    $F' \leftarrow F_n(X) \wedge G_n(Y)$ 
3    $Q \leftarrow$  a sequence with all clauses of  $F_n(X)$ 
4    $J \leftarrow 0$ 
5   for  $k$  in  $1..s$  do
6     if  $C_k$  is absorbed by  $F'$  then
7       continue
8     Continue CDCL execution on the  $Y$  variables
9     as in Lemma 1 until  $y_0$  is assigned a value
10    Let  $d$  be the current decision level
11    while  $C_k$  is not absorbed by  $F'$  do
12      Let  $\ell \in C_k$  be a literal witnessing that  $C_k$  is
13      not absorbed by  $F'$ 
14      repeat
15        Branch on  $\bar{\ell}$  for all  $\ell' \in C_k \setminus \{\ell\}$ 
16        Branch on  $\bar{\ell}$  at decision level  $d + |C_k|$ 
17        Realize a conflict involving only  $X$ 
18        Derive an asserting clause  $C$  over  $X$ 
19        Append to  $Q$  resolution proof of  $C$ 
20        from  $F'$  given by the conflict graph
21         $F' \leftarrow F' \wedge C$ 
22         $b \leftarrow$  asserting level of  $C$  (0 or  $> d$ )
23        Backjump to decision level  $b$ 
24      until  $C_k$  is absorbed by  $F'$  w.r.t.  $\ell$ 
25      if  $b > d$  then
26        Branch on  $\bar{v}$  at level  $b + 1$  (continues
27        execution on  $G_n(Y)$  as in Lemma 1)
28        Learn clause 1-equivalent to  $D_{2J+1}$ 
29        with asserting level  $d$ 
30        Backtrack to decision level  $d$ 
31        Learn clause 1-equivalent to  $D_{2J+2}$ 
32        with asserting level  $d - 1$ 
33        Backtrack to decision level  $d - 1$ 
34         $J \leftarrow J + 1$ 
35    Backtrack to decision level 0; Realize a conflict
36    return  $Q$ 
37 end

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Proof. If $\text{length}(P) > (2^n - 1)/n^2$, the result follows from Lemma 2. Otherwise we proceed as follows. We assume w.l.o.g. that P is minimal. We observe that since $G_n(Y)$ is satisfiable and on disjoint variables from $F_n(X)$, any minimal resolution refutation of $F_n(X) \wedge G_n(Y)$ must be a refutation of $F_n(X)$.

The CDCL solver execution is given by Algorithm 1. The simulation begins (Line 2) by setting F' to $F_n(X) \wedge G_n(Y)$. The idea, following the PD simulation, is to have F' absorb each of the inferred clauses one by one. When the outer for-loop (Line 5) of the algorithm finishes, F' must have absorbed the empty clause and hence produced a resolution

refutation of $F_n(X) \wedge G_n(Y)$ and thus of $F_n(X)$.

We start (Line 8) by branching on the Y variables as in Lemma 1 until y_0 is assigned a value at some decision level d . In order to absorb a clause C_k that is currently not absorbed by F' , the simulation identifies a literal $\ell \in C$ witnessing that C_k is not yet absorbed (Line 11) and then branches negatively on all literals of C_k other than ℓ (Line 13). Since C is not absorbed, making these $|C_k| - 1$ branching decisions will be possible in spite of eager unit propagation, will not cause a conflict by unit propagation, and will allow branching on $\bar{\ell}$. This last branch (Line 14), however, will cause a conflict because C_1, \dots, C_{k-1} are already absorbed by F' . From this conflict, S will learn an asserting clause C (Line 16) whose resolution derivation involves ℓ . Since these clauses are only derived from $F_n(X)$, C must be a clause over X . If the asserted literal in C is different from ℓ and C_k remains unabsorbed w.r.t. ℓ , this process of branching on $\bar{\ell}$ is repeated (Lines 12-21) no more than $n - |C_k| + 1$ times until ℓ becomes the asserted literal (this holds because all asserted variables here must be distinct and cannot belong to $C_k \setminus \{\ell\}$). S will backtrack to some asserting level b (Line 19), which must either be the level of some X variable or be 0 (if S learns a unit clause). At this point, we have absorbed C_k w.r.t. ℓ . If $b > d$, however, the “leftover” branching decisions on the X variables of C_k may interfere with the derivation of clauses needed to absorb C_k w.r.t. other literals or to absorb C_{k+1}, \dots, C_s . In order to avoid this situation, the PD simulation simply restarts the solver and forcibly returns to decision level 0. Since S is a non-restarting CDCL solver, our construction employs the counter involving the Y variables and a decision setting $v = 0$ (Line 23) to backtrack over any leftover branching decisions on the X variables that remain after absorbing C_k . This increments the counter and brings us back to decision level $d - 1$ (Lines 24-27).

Repeating this process (Lines 10-28) for other literals of C_k witnessing that C_k is not yet absorbed will eventually result in C_k being absorbed. Thus, after no more than $(n - |C_k| + 1)|C_k|$ repetitions, each of which involves at most $|C_k|$ branching decisions or at most n resolution steps, we are able to absorb C_k . This yields a total of at most $n \sum_{k=1}^s |C_k|^2$ branching decisions and at most $n^2 \sum_{k=1}^s |C_k| = n^2 \cdot \text{size}(P)$ resolution steps, to absorb all clauses of P .

The total number R of restarts needed to be simulated is at most $n^2 \cdot \text{length}(P)$ which, by our earlier argument, is at most $2^n - 1$. Hence, $\log_2(R + 1) \leq n$ and, from Theorem 1, it suffices to use $G_n(Y)$ as the counter for $F_n(X)$. \square

Concluding Remarks

Known efficient simulations of resolution refutations by modern CDCL SAT solvers that learn one asserting clause per conflict have so far relied heavily on the ability of such solvers to restart several times per resolution derivation step that they are trying to simulate. Our result shows that simple preprocessing and an associated branching heuristic can replace the need for these solvers to explicitly restart. As a consequence, from a theoretical standpoint, such solvers

are in fact powerful enough to efficiently simulate resolution refutations without relying on restarts at all.

The result holds for all CDCL solvers that learn (at least) one asserting clause from each conflict, irrespective of whether or not they employ other common techniques such as backjumping and phase saving. As remarked briefly earlier, the construction can be extended to withstand simplification techniques such as pure literal elimination.

The kind of preprocessing used in our non-restarting simulation is admittedly of a rather different nature than the commonly used preprocessing techniques geared towards increasing the efficiency of SAT solvers. It adds a counting formula on a brand new set of variables such that branching first on (a subset of) these new variables enables the solver to freely backtrack out of any “leftover” partial assignment after realizing a conflict.

While our simulation result answers an open theoretical question, research on effective restart strategies continues to be relevant for CDCL solvers in practice. The question of whether modern CDCL solvers can polynomially simulate resolution proofs without restarting and without preprocessing the input formula remains open.

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